Eric Hauser

ПІП

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About me

- M.Sc. Electrical Engineering and Information Technology (TUM)
- Student exchanges:
 - The Hong Kong University of Science and Technology
 - The University of Tokyo (partly canceled due to COVID-19 pandemic)
- Finished in September 2021 and started PhD shortly after
- PhD at the Chair of Network Architectures and Services (Prof. Carle)

Research interests

- P4 programmable data planes (especially *Intel Tofino* platform)
- Measurements and modeling of packet processing systems
- P4 powered applications in 5G/6G core networks



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Research Questions for Discussion

- 5G/6G core: How to achieve reliability, scalability, and performance of programmable data planes?
- Image a new Intel Tofino, what should be improved and which new features should be implemented?
- How to implement (secure) In-Network Computing on P4 programmable hardware targets?